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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroyuki NANSEI et al.**

Serial Number: **10/643,967**

Application Division

Filed: **August 20, 2003**

Customer No.: 38834

For: **SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR
MANUFACTURING SEMICONDUCTOR DEVICE**

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: June 17, 2004

Sir:

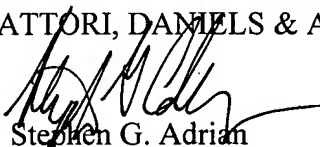
Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the Official Filing Receipt, we noted an error in the **Assignee City**. The **Assignee City** should read: --**AIZUWAKAMATSU-SHI**--. A copy of the Assignment is enclosed which indicates the correct information. We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP


Stephen G. Adrian
Attorney for Applicants
Reg. No.: 32,878

Atty. Docket No.: **030993**
1250 Connecticut Avenue N.W.,
Suite 700,
Washington, D.C. 20036
(202) 822-1100

SGA/rf
Enclosures: Official Filing Receipt; Assignment



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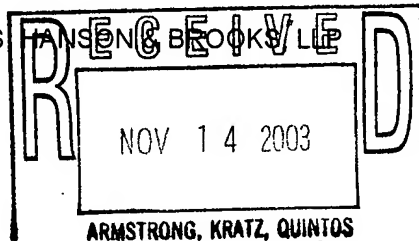
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/643,967	08/20/2003	2812	1242	030993	36	38	5

CONFIRMATION NO. 4992

23850

ARMSTRONG, KRATZ, QUINTOS
 1725 K STREET, NW
 SUITE 1000
 WASHINGTON, DC 20006



FILING RECEIPT



OC000000011263879

Date Mailed: 11/13/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent ApplicationFUJITSU AMD SEMICONDUCTOR LIMITED, ~~Aizuwakamatsu-shi~~, JAPAN;

Aizuwakamatsu-shi

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-256195 08/30/2002

If Required, Foreign Filing License Granted: 11/13/2003

Projected Publication Date: 03/04/2004

Non-Publication Request: No

Early Publication Request: No



Title

Semiconductor memory device and method for manufacturing semiconductor device

Preliminary Class

438

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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U.S. ASSIGNMENT(Insert ASSIGNEE's
Name(s) Address(es))

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by
FUJITSU AMD SEMICONDUCTOR LIMITED, 6, Kogyodanchi,

Monden-machi, Aizuwakamatsu-shi, Fukushima 965-0845 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned
 ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title
 and interest to the invention entitled

(Title of Invention)

SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR MANUFACTURING

SEMICONDUCTOR DEVICE

(*If the assignment is
 being filed after the
 filing of the
 application, this
 section must be
 completed)

for which application for Letters Patent of the United States was executed on even date herewith
 unless otherwise indicated below:

filed on _____, Serial No. _____.

(ARMSTRONG, WESTERMAN & HATTORI, LLP is hereby authorized to insert the series code, serial
 number and/or filing date hereon, when known)

and all Letters Patent of the United States to be obtained therefore on said application or any
 continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for
 which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any
 continuation, divisional, reissue or reexamination applications thereof and also to execute separate
 assignments in connection with such applications as the ASSIGNEE may deem necessary or
 expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference,
 litigation, or other legal proceeding which may be declared concerning this application or any
 continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued
 thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing
 evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their
 signature(s).

(Signatures)

<u>Hiroyuki Nansei</u> (Signature)	<u>Hiroyuki NANSEI</u> (Type Name)	<u>Jul 14, 2003</u> (Date)
<u>Manabu Nakamura</u> (Signature)	<u>Manabu NAKAMURA</u> (Type Name)	<u>Jul 14, 2003</u> (Date)
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